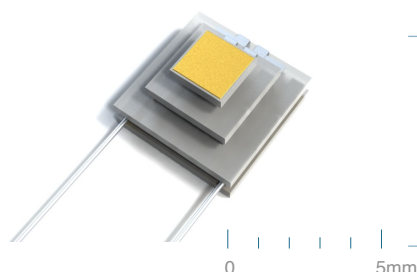## THERMOELECTRIC COOLER PERFORMANCE
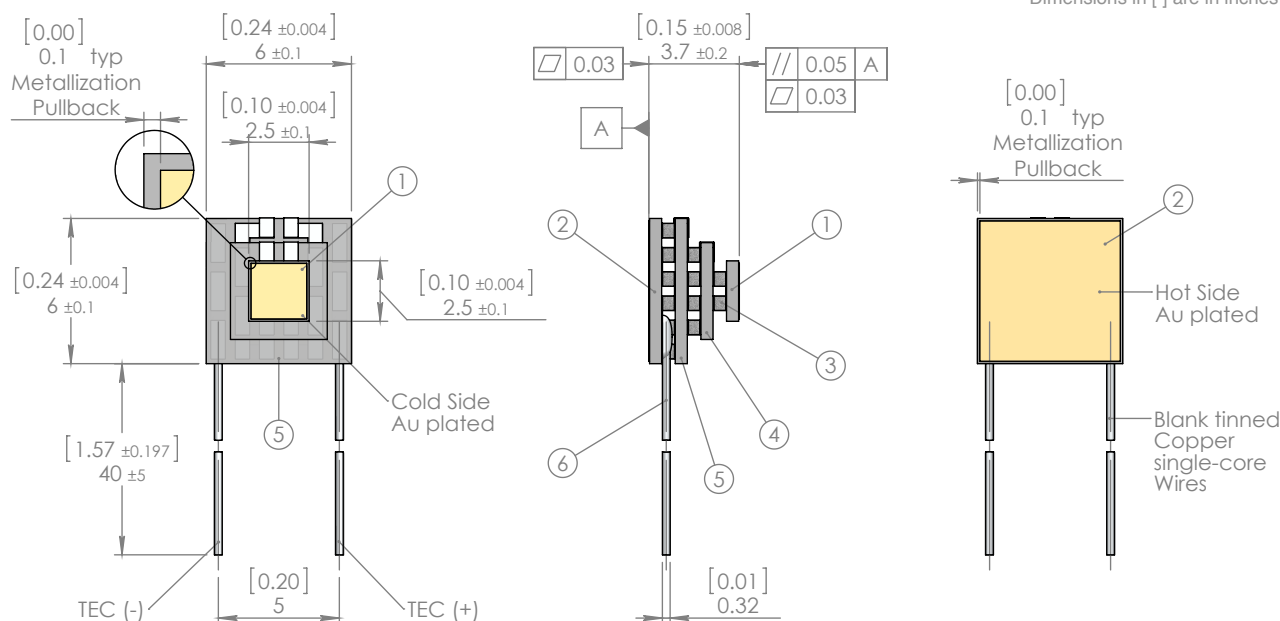
$\Delta Tmax$ K	Qmax W	Imax A	Umax V	ACR Ohm	Ambient Temperature	Conditions
113	0.58	2.3	1.9	0.7	+27ºC / 300K	Vacuum
115	0.66	2.3	2.2	0.8	+50ºC / 323K	Dry N2
128	0.72	2.3	2.4	0.9	+75ºC / 348K	Dry N2
132	0.74	2.3	2.5	0.9	+85ºC / 358K	Dry N2

0   5mm

Note: Thermoelectric Cooler performance values are specified for optimal conditions, assuming that TEC hot side (T_{hot}) is stabilized at ambient temperature (T_{amb})

TECHNICAL DRAWING   3MC06-024-05

Dimensions are in mm
Dimensions in [ ] are in inches

[0.00]
0.1 typ
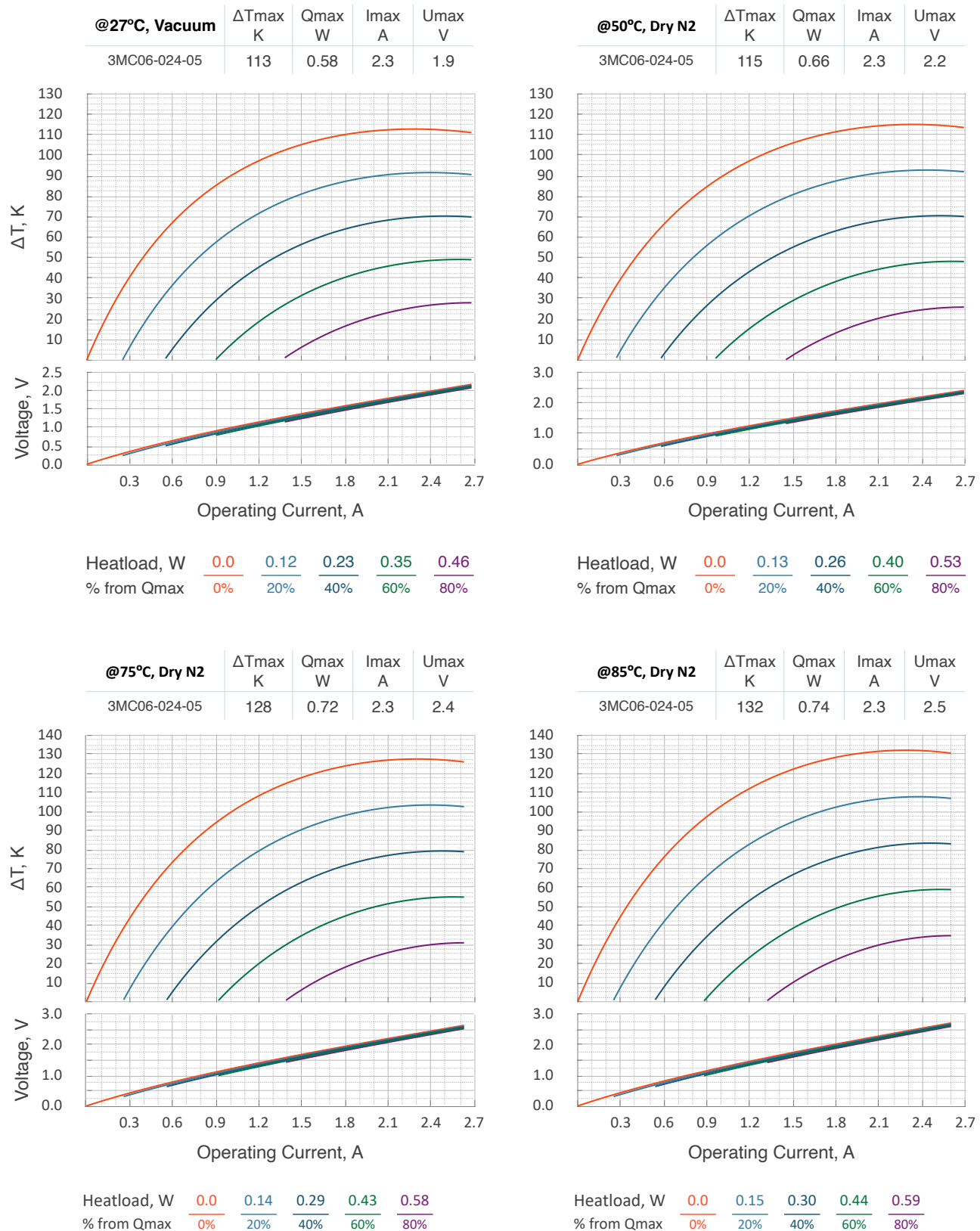Metallization Pullback

[0.24 ±0.004]
6 ±0.1

[0.10 ±0.004]
2.5 ±0.1

[0.24 ±0.004]
6 ±0.1

[0.10 ±0.004]
2.5 ±0.1

[1.57 ±0.197]
40 ±5

[0.20]
5

TEC (-)   TEC (+)

Cold Side Au plated

[0.15 ±0.008]
3.7 ±0.2

0.03   // 0.05 A
A   0.03

[0.01]
0.32

[0.00]
0.1 typ
Metallization Pullback

Hot Side Au plated

Blank tinned Copper single-core Wires

TEC DESCRIPTION

- Ceramics: Al_2O_3 (100%)
- Internal Assembly: Solder Sn-Sb (T_{melt}=230ºC)
- Cold Side (1) Surface: Au plated (0.2 - 0.3um)
- Hot Side (2) Surface: Au plated (0.2 - 0.3um)
- Terminal Contacts (6): AWG-28 Wires, blank
- TEC Polarity: standard
- Bi-Te Material (3): high-grade, hot-extruded type
- Protective Coating: N/A (available by request)
- Integrated Thermistor: N/A (available by request)

KEY FEATURES

- Regular pellets placement technology
- RoHS EU Compliant
- REACH EU Compliant
- TELCORDIA GR-468 (MIL-883) qualified
- Classical "pyramidal" multistage TEC shape
- WB configuration is available by request
- Up to 225ºC short time processing (for mounting)
- Wide range of additional manufacturing options (see Pages 3,4)

PERFORMANCE PLOTS

@27°C, Vacuum	ΔTmax K	Qmax W	Imax A	Umax V
3MC06-024-05	113	0.58	2.3	1.9

@50°C, Dry N2	ΔTmax K	Qmax W	Imax A	Umax V
3MC06-024-05	115	0.66	2.3	2.2

Heatload, W	0.0	0.12	0.23	0.35	0.46
% from Qmax	0%	20%	40%	60%	80%

Heatload, W	0.0	0.13	0.26	0.40	0.53
% from Qmax	0%	20%	40%	60%	80%

@75°C, Dry N2	ΔTmax K	Qmax W	Imax A	Umax V
3MC06-024-05	128	0.72	2.3	2.4

@85°C, Dry N2	ΔTmax K	Qmax W	Imax A	Umax V
3MC06-024-05	132	0.74	2.3	2.5

Heatload, W	0.0	0.14	0.29	0.43	0.58
% from Qmax	0%	20%	40%	60%	80%

| Heatload, W	0.0	0.15	0.30	0.44	0.59
% from Qmax	0%	20%	40%	60%	80%

Note: Thermoelectric cooler performance values and plots are specified at optimal conditions, assuming TEC hot side is stabilized at ambient temperature ($T_{hot}=T_{amb}$). The performance data is specified for four most common ambient condition modes. Please, contact TEC Microsystems GmbH directly for estimations under different conditions, if required.

THERMOELECTRIC COOLER TERMINAL CONNECTION OPTIONS
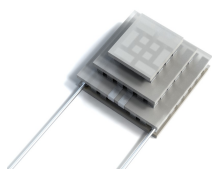
◉ Blank Wires
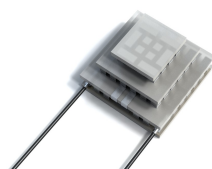
☐ Insulated Wires
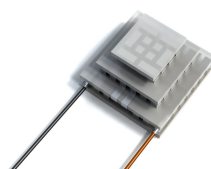
☐ Insulated Color-coded Wires
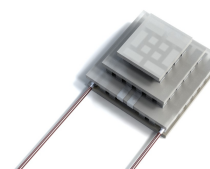
☐ Varnished Wires
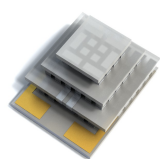
☐ Front Pads WB
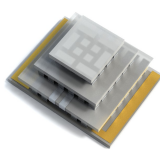
☐ Side Pads WB
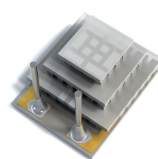
☐ Vertical Rods
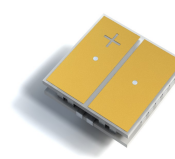
☐ SMD / Flip-chip (contacts on hot side)

● - By default thermoelectric cooler is provided with blank tinned single-core Copper wires. Various TEC terminal connection options are available by request. In the case of a thermoelectric cooler with wires, customer can select the wire type, diameter, and length.

THERMOELECTRIC COOLER SURFACE OPTIONS

Standard ceramics surface options are available without price change.
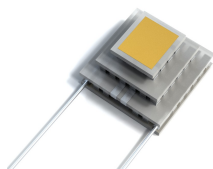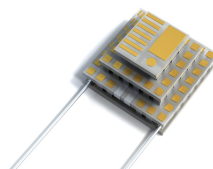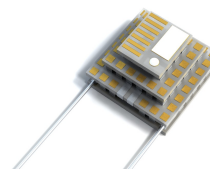Pre-tinning can be applied to one side of the TEC or both. Different solder types are available for pre-tinning.
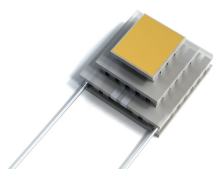
◉ Au plated
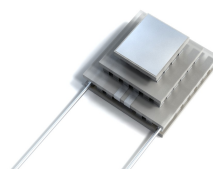
☐ Pre-tinned with solder
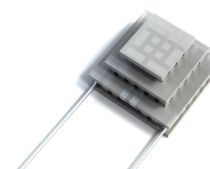
☐ Blank ceramics (w/o metallization)

Advanced ceramics surface options, available by request. Surface customization can be applied to one side of the TEC or both.

☐ Custom gap

☐ Customized Au pattern

☐ Selective pre-tinning over pattern

● - By default thermoelectric cooler is provided with Au plated ceramics surfaces (both sides). Au plated surface is the universal solution, suitable for soldering and gluing. Surface type and configuration can be specified individually for each TEC side (top and bottom ceramics surfaces).

3MC06 THREE-STAGE THERMOELECTRIC COOLER OVERVIEW

Top Ceramics
(Cold Side)

Middle Ceramics 2

Middle Ceramics 1

Terminal Wires
(by default)

Stage
Connectors

Bottom
Ceramics
(Hot Side)

Internal
Metallization
junctions

BiTe posts
(P-N couples)
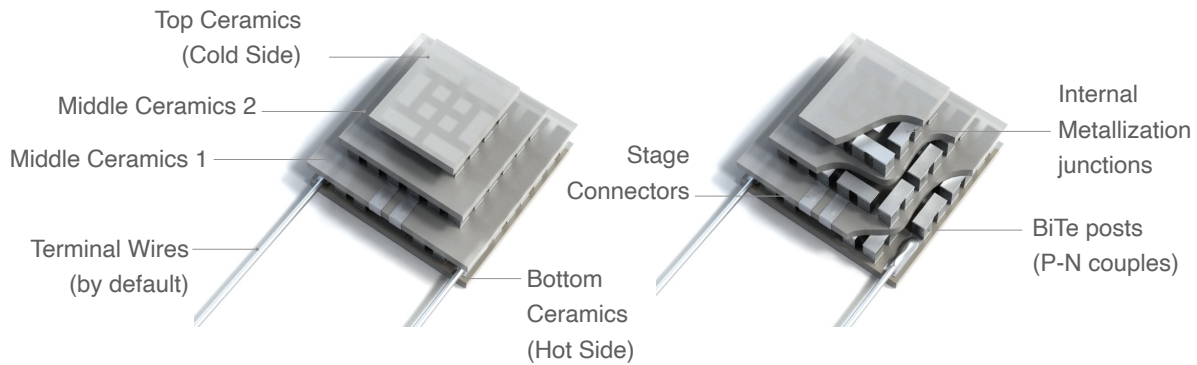
- Multistage thermoelectric coolers with classical "pyramidal" shape.
- TEC configurations with WB pads or WB posts are possible but may require TEC hot side modification.
- TEC internal Assembly Solder by default: Sn-Sb, T_{melt}=230ºC

RoHS Compliant

APPLICATION TIPS

- Maximum **short time process** temperature is 220˚C (TEC assembly solder has T_{melt}=230˚C).
- Maximum **long time operating** temperature is 150˚C.
- Use thermoelectric cooler only with an appropriate heat sink attached to the hot side.
- Connect thermoelectric cooler to DC power supply in according to TEC polarity.
- Do not exceed DC current and voltage higher than Imax and Umax values.
- Use temperature ramping and avoid thermal shocks during soldering and/or baking processes.
- Check FAQ on TEC Microsystems web for more details about TECs operating and handling.

220

150

| Room Temperature | | +90ºC | +150ºC |

| Normal | Optimal Temperature for TEC long-time operating | Acceptable | Critical |
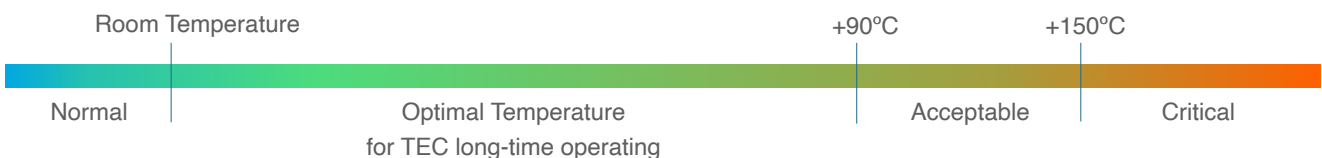
TEC Lifetime depends on Ambient Temperature (T_{amb}) and can be estimated by Arrhenius equation

Optimal T_{amb} — ... ~ 300000 hours

T_{amb}=150ºC — ~10000 hours

T_{amb}=200ºC — ~100 hours
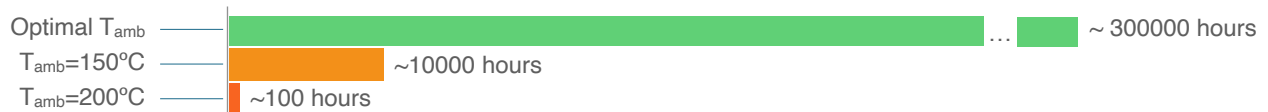
Term "Lifetime" for TEC is taken from Telcordia GR-468 Standard. The criteria of failure is TEC AC Resistance (ACR) change for more than 5%. It doesn't mean TEC stops operating, but certain performance degradation appears.

IMPORTANT NOTES

1. Thermoelectric Cooler (TEC) performance in this datasheet is specified in typical ambient condition modes (Vacuum, +27ºC; Dry N2, +50ºC; Dry N2, +75ºC and Dry N2, +85ºC. TEC performance may differ under other conditions. Please, contact TEC Microsystems for detailed analysis and additional TEC performance info.

2. TEC ACR and Umax values are sensitive to ambient temperature. These values can be different from those specified in the datasheet at other ambient conditions. ACR and Umax rise with ambient temperature increasing.

3. TEC dTmax is specified at zero heatload, while Qmax is specified at zero dT (check Fig.1 for example). TEC dTmax and Qmax values rise with ambient temperature (check Fig. 2 for example).

4. Thermoelectric coolers have the best performance in the temperature range from near room up to +80..90ºC. TEC cooling performance is getting lower at ambient temperatures below 0ºC. TECs are not suitable to operate at cryogenic temperatures.
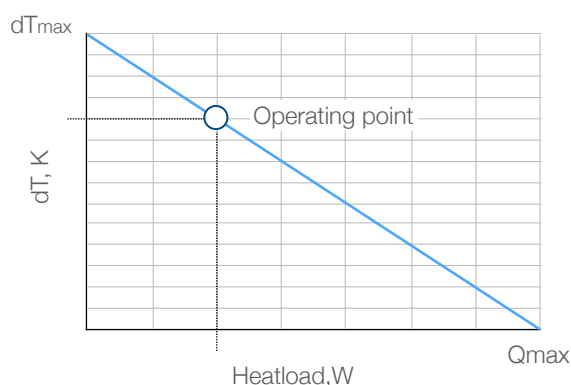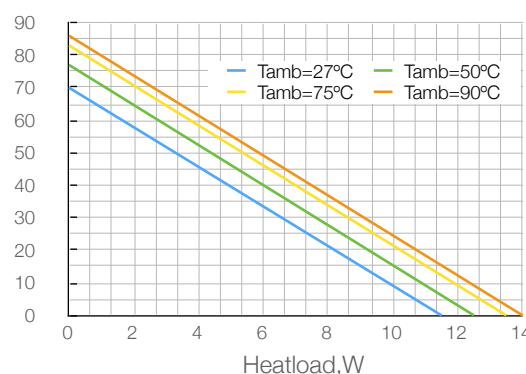
Fig. 1 - Understanding dTmax and Qmax

Fig. 2 - Same TEC dTmax and Qmax parameters at different ambient temperatures

5. Driving TEC at Imax or Umax level doesn't mean the max cooling performance mode. The real optimal mode may depend on operating conditions, required dT level and application heatload. In fact a better performance can be reached at operating current and voltage lower than Imax and Umax values specified in datasheet.

6. It is strongly recommended to avoid a direct mounting of the thermoelectric cooler to pure Copper, Aluminium or Nickel materials and a mounting of objects from these materials on the TEC cold side. Any material with high CTE (Coefficient of Thermal Expansion) may affect TEC lifetime and even damage TEC in case of improper mounting, thermal shock or temperature cycling. In case of the necessity of the material as mentioned above, it is recommended to use some elastic "soft" solders or glues with a large modulus of elasticity (Indium-based solders or silicon-based thermoconductive glues).

7. TEC Microsystems GmbH confirms that all thermoelectric coolers are qualified and meet the requirements of Telcordia GR-468 Standard (based on MIL-883). The up-to-date Reliability Report is available by request. TEC Microsystems GmbH warranties thermoelectric coolers lifetime no less than 250K-300K operating hours under normal application conditions.

8. Additional information about thermoelectric coolers operating and handling, all the most important questions and answers, are available on TEC Microsystems web in FAQ section (link).

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